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APPLICANTS

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** CONTINUING DATA none *me*

** FOREIGN APPLICATIONS yes *me*
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 6
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Verified and Acknowledged
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TITLE
 Semiconductor integrated circuit and scan test method therefor

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